ABSTRACT

An apparatus for correction of a resampler is provided, with which a sampled input signal, that is subjected to an input sampling rate and which has a chip frequency that differs from the input sampling rate, is converted into a sampled output signal for which the sampling rate corresponds with the chip frequency, by changing the input sampling rate by a resampling factor. The input signal is subjected to a non-linear operation so that a spectral line is produced at the chip frequency. A frequency shifter spectrally shifts the input signal by the chip frequency. The phase of the shifted spectral line at the chip frequency is determined as a function of sampling time points. On the basis of a regression of the phase of the shifted spectral line at the chip frequency, the resampling factor is corrected and/or the output signal is timewise shifted by a time correction value.